

STPS30120C

Power Schottky rectifier

Main product characteristics

I _{F(AV)}	2 x 15 A
V _{RRM}	120 V
T _{j(max)}	175° C
V _{F(typ)}	0.57 V

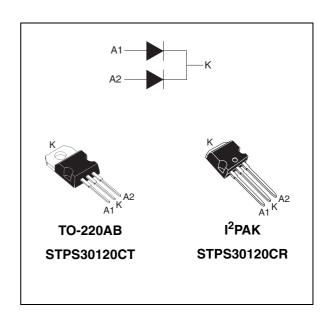
Feature and benefits

- High junction temperature capability
- Avalanche rated
- Low leakage current
- Good trade-off between leakage current and forward voltage drop

Description

Dual center tap Schottky rectifier suited for high frequency switch mode power supply.

Packaged in TO-220AB and I²PAK, this device is intended to be used in notebook and LCD adaptors, desktop SMPS, providing in these applications a margin between the remaining voltages applied on the diode and the voltage capability of the diode.



Order code

Part Number	Marking
STPS30120CT	STPS30120CT
STPS30120CR	STPS30120CR

Table 1. Absolute ratings (limiting values, per diode)

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Symbol		Value	Unit		
V_{RRM}	Repetitive peak reverse voltage			120	٧
I _{F(RMS)}	RMS forward voltage			30	Α
$I_{F(AV)}$	Average forward current	δ = 0.5 Tc = 145° C	Per diode Per device	15 30	Α
I _{FSM}	Surge non repetitive forward current		t _p = 10 ms Sinusoidal	180	Α
P _{ARM}	Repetitive peak avalanche power t _p =		$t_p = 1 \ \mu s \ T_j = 25^{\circ} \ C$	6700	W
T _{stg}	Storage temperature range Maximum operating junction temperature ⁽¹⁾			-65 to + 175	° C
Tj				175	° C

^{1.} $\frac{dPtot}{dT_i} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

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Table 2. Thermal parameters

Symbol	Parameter		Value	Unit
R _{th(j-c)}	Junction to case	Per diode Total	2.2 1.3	° C/W
R _{th(c)}	Coupling	Total	0.3	° C/W

When the diodes 1 and 2 are used simultaneously:

 $T_i(diode 1) = P(diode 1) \times R_{th(i-c)}(per diode) + P(diode 2) \times R_{th(c)}$

Table 3. Static electrical characteristics (per diode)

Symbol	Test conditions			Min.	Тур.	Max.	Unit
ı (1)	$I_R^{(1)}$ Reverse leakage current $T_j = 25^{\circ} C$ $T_j = 125^{\circ} C$ $V_R = V_{RRM}$			15	μΑ		
'R`		T _j = 125° C	$v_R = v_{RRM}$		2.5	7.5	mA
	$V_{F}^{(2)} \text{Forward voltage drop} \begin{array}{c} T_{j} = 25^{\circ} C \\ T_{j} = 125^{\circ} C \\ \hline T_{j} = 25^{\circ} C \\ \hline T_{j} = 125^{\circ} C \\ \hline \end{array} \begin{array}{c} I_{F} = 5 A \\ \hline \end{array} \begin{array}{c} -15 A \\ \hline \end{array} $			0.74			
		T _j = 125° C	IF = 3 A		0.57	0.61	
V (2)		T _j = 25° C	I _F = 15 A			0.92	V
VF` ′		T _j = 125° C			0.7	0.74] v
		T _j = 25° C	I _F = 30 A			1.02	
		T _j = 125° C			0.83	0.89	

^{1.} Pulse test : tp = 5 ms, δ < 2%

To evaluate the maximum conduction losses use the following equation :

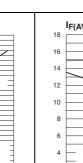
$$P = 0.59 \times I_{F(AV)} + 0.01 I_{F}^{2}_{(RMS)}$$

^{2.} Pulse test : tp = 380 μ s, δ < 2%

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Figure 2.

Figure 1. Average forward power dissipation versus average forward current (per diode)



Average forward current versus ambient temperature (δ = 0.5, per diode)

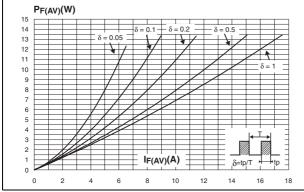
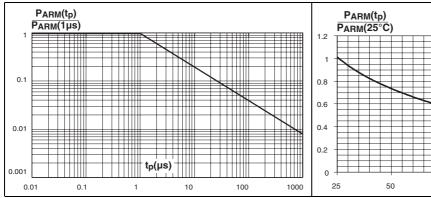


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature



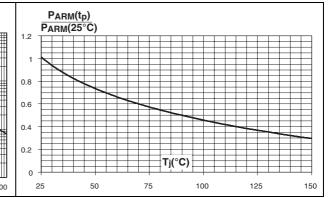
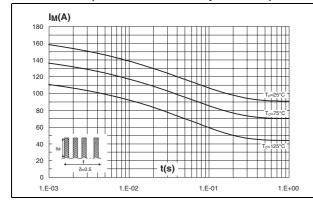
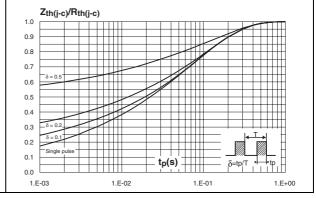


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values, per diode)

Figure 6. Relative variation of thermal impedance junction to ambient versus pulse duration

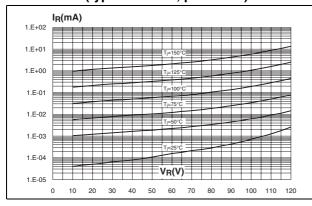




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Figure 7. Reverse leakage current versus reverse voltage applied (typical values, per diode)

Figure 8. Junction capacitance versus reverse voltage applied (typical values, per diode)



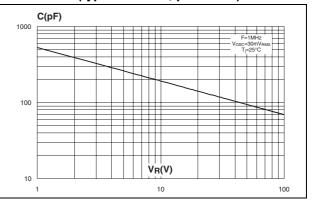
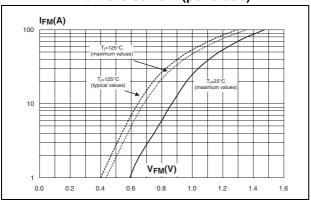


Figure 9. Forward voltage drop versus forward current (per diode)



STPS30120C **Package information**

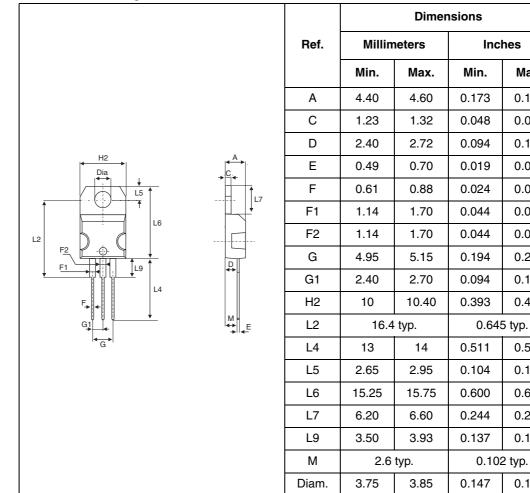
Package information 2

Epoxy meets UL94, V0

Cooling method: by conduction (C) Recommended torque value: 0.8 Nm.

Maximum torque value: 1.0 Nm.

Table 4. Package dimensions TO-220AB



Max.

0.181

0.051

0.107

0.027

0.034

0.066

0.066

0.202

0.106

0.409

0.551

0.116

0.620

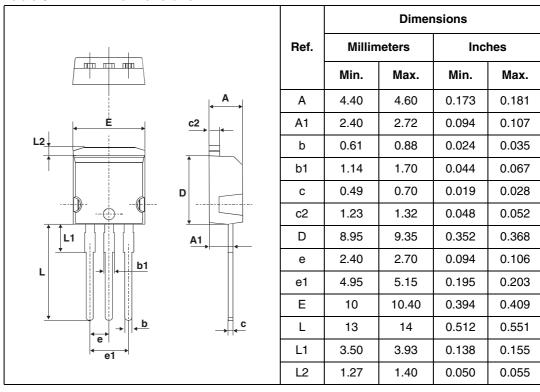
0.259

0.154

0.151

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Table 5. I²PAK dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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3 Ordering information

	Ordering type	Marking	Package	Weight	Base qty	Delivery mode
Ī	STPS30120CT	STPS30120CT	TO-220AB	2.23 g	50	Tube
Ī	STPS30120CR	STPS30120CR	I ² PAK	1.49 g	50	Tube

4 Revision history

rds	Revision	Description of Changes
18-Feb-2005	1	First issue
23-Nov-2006	2	Reformatted to current standards. Added I ² PAK package

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